

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

## Title of Invention

SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING  
CLOCK FEEDTHROUGH EFFECT AND HAVING LOW PHASE  
NOISE AND METHOD THEREOF

Application Number :

Confirmation Number:

First Named Applicant: En-Hsiang Yeh

Attorney Docket Number: MTKP0157USA4

Art Unit:

Examiner:

Search string: ( 6226506 ).pn

## US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6226506	2001-05-01	Welland , et al.			

## Signature

Examiner Name	Date